# Computer Memory Test Labs: Intel Advanced Final Report

<table>
<thead>
<tr>
<th>MOTHERBOARD</th>
<th>MANUFACTURER</th>
<th>CHIPSET</th>
<th>TEST NUMBER</th>
<th>TEST STATUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1200KPR</td>
<td>Intel</td>
<td>Intel® C206</td>
<td>018858</td>
<td>PASSED</td>
</tr>
</tbody>
</table>

## Module Information

- **Manufacturer Name:** Dataram
- **Part #:** DTM64395A
- **MB:** 4GB
- **Config:** 512M x 72
- **DRAM:** Hynix
- **DRAM Part #:** H5TQ2G83CFR-PBC rev C
- **PCB Part:** 40090A rev A
- **PCB_layer_count:** 8 Layer
- **Module Info:** Unbuffered ECC

## Minimum System Information

<table>
<thead>
<tr>
<th>Minimum System</th>
<th>S1200KPR</th>
<th>Serial Number</th>
<th>BTPK22401NNH</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIOS at time of test</td>
<td>KPC2060H.86B.0023.2012.07</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Processor Code</td>
<td>Intel® Xeon® CPU E3-1275 V2 @ 3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Memory Count</td>
<td>4GB</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Event Log Verify</td>
<td>Clear</td>
<td></td>
<td></td>
</tr>
<tr>
<td>OS Memory Count</td>
<td>4GB 502M</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Software Pack</td>
<td>N/A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Disk Manuf.</td>
<td>R.S.T PRO3 v5.33</td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
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<td></td>
<td></td>
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</tbody>
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## Pre-Testing Information

<table>
<thead>
<tr>
<th>Board Revision</th>
<th>G34877-201</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Test</td>
<td>Pass</td>
</tr>
<tr>
<td>SPD Check</td>
<td>Pass</td>
</tr>
<tr>
<td>Speed Check</td>
<td>Pass</td>
</tr>
<tr>
<td>Power Cycle Stat</td>
<td>Pass</td>
</tr>
<tr>
<td>Power Cycle Time</td>
<td>100</td>
</tr>
</tbody>
</table>

## Voltage Margining Info

| Volt % | N/A |
| Volt Low | N/A |
| Volt High | N/A |

## Testing Details for Phase 2

- **Temperature:** 55°C
- **Time:** 12Hr
- **Start Date:** 9/12/12
- **Stop Date:** 9/17/12
- **Min Memtest v2.11 Status:** Pass
- **Max Memtest v2.11 Status:** Pass
- **SEL Min:** Pass
- **SEL Max:** Pass
- **Summary Min:** Pass
- **Summary Max:** Pass
- **Test Phase Status:** Pass

## Testing Details for Phase 3

- **Temperature:** 55°C
- **Time:** 12Hr
- **Start Date:** 9/12/12
- **Stop Date:** 9/17/12
- **Min Memtest v2.11 Status:** Pass
- **Max Memtest v2.11 Status:** Pass
- **SEL Min:** Pass
- **SEL Max:** Pass
- **Summary Min:** Pass
- **Summary Max:** Pass
- **Test Phase Status:** Pass

Test Traveler Notes:

- Min configuration test 24hr, 47 Loops Passed, 0 Error; Memory speed running @ 1600MHz
- Max configuration test 24hr, 31Loops Passed, 0 Error; Memory speed running @ 1600MHz
- RST PRO3 V5.33 was used for this test.

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