## Computer Memory Test Labs
### Intel® Advanced Tested Final Report

<table>
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<tr>
<th>MOTHERBOARD</th>
<th>MANUFACTURER</th>
<th>CHIPSET</th>
<th>TEST NUMBER</th>
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<tr>
<td>DH87RL</td>
<td>Intel</td>
<td>Intel® H87</td>
<td>021061</td>
<td>PASSED</td>
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### MEMORY MODULE INFORMATION
- **Manufacturer Name:** Kingston
- **Manufacturer Part #:** KVR16N11S8H/4
- **DRAM Manufacturer:** Elpida
- **DRAM Part #:** EDJ4208BBBG-GN-F rev B
- **Module Config:** 512M x 64
- **Module Size:** 4GB
- **PCB Part #:** 2025402-0F1.D00 rev D
- **PCB Layer Count:** 6 Layer
- **Module Information:** Unbuffered 1.5V, Non-ECC, 1600MHz, DDR3 DIMM
- **CMTL Part #:** KIN353

### SYSTEM INFORMATION

#### MINIMUM SYSTEM INFORMATION
- **Minimum System:** DH87RL
- **Serial Numbe:** BQR315000BV
- **Board Revision:** G74240-400
- **Bios Memory Count:** 8GB
- **Processor Code:** Intel® Core™ i5-4570T @ 2.90GHz
- **Bios at time of test:** RLH8710H.86A.0317.2013.0426.1724
- **RST PRO Count:** 8G 510M

#### MAXIMUM SYSTEM INFORMATION
- **Maximum System:** DH87RL
- **Serial Numbe:** BQR315000E1
- **Board Revision:** G74240-400
- **Bios Memory Count:** 16GB
- **Processor Code:** Intel® Core™ i5-4570T @ 2.90GHz
- **Bios at time of test:** RLH8710H.86A.0317.2013.0426.1724
- **R.S.T PRO Count:** 16G 510M

### TESTING DETAILS
- **55°C Temperature / Standard Voltage**
- **Start Date:** 08/19/2013
- **Stop Date:** 08/21/2013
- **Insertion Test:** Pass
- **SPD Check:** Pass
- **Minimum Load:** Pass
- **Maximum Load:** Pass
- **S0 ~ S5 Test:** Pass
- **Power Cycle Test:** Pass
- **Test Card Version:** R.S.T Premium v6.05

### Test Notes:
- Min configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1600MHz
- Max configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1600MHz

Microsoft Windows-7 64bit version is used as it support over 4GB memory and must support target chipset/CPU platform. All board drivers must be loaded when drivers are available on Intel web site. Front panel power cycling test, ACPI S3 sleep state cycling test, and Hibernate S4/S5 state cycling test is done up to 20 cycle each when the board support it. At least 2 loops of all test under R.S.T PRO3 to be completed.

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