### MEMORY MODULE INFORMATION

Manufacturer Name: **Legacy Electronics Inc.**
Manufacturer Part #: **LE38US13JAM-EB100-000L0**
DRAM Manufacturer: **Micron**
DRAM Part #: **MT41K512M8RH-125:E rev E**
Module Config: **1024M x 64**
Module Size: **8GB**
PCB Part #: **D3U28B-B1**
PCB Layer Count: **6 Layer**
Module Information: **Unbuffered 1.35V Non-ECC 1333MHz DDR3 DIMM**

### MINIMUM SYSTEM INFORMATION

- **Bios at time of test:** RLH8710H.86A.0322
- **Processor Code:** Intel® Core™ i5-4570T @ 2.90GHz
- **Bios Memory Count:** 16GB
- **Bios Memory Count:** 16GB

### MAXIMUM SYSTEM INFORMATION

- **Bios at time of test:** RLH8710H.86A.0322
- **Processor Code:** Intel® Core™ i5-4570T @ 2.90GHz
- **Bios Memory Count:** 32GB
- **Bios Memory Count:** 32GB

### SYSTEM INFORMATION

- **Assembly Type:** JEDEC Standard
- **Environment Code:** RoHS
- **Low Profile:** 30mm LP
- **Bank:** 2

### TESTING DETAILS

- **55°C Temperature / Standard Voltage**
  - **Start Date:** 11/01/2013
  - **Stop Date:** 11/20/2013
- **Insertion Test:** Pass
- **SPD Check:** Pass
- **Minimum Load:** Pass
- **Maximum Load:** Pass
- **S0 ~ S5 Test:** Pass
- **Power Cycle Test:** Pass
- **Test Card Version:** R.S.T Premium v6.05

**Microsoft Windows-7 64bit version is used as it support over 4GB memory and must support target chipset/CPU platform. All board drivers must be loaded when drivers are available on Intel web site. Front panel power cycling test, ACPI S3 sleep state cycling test, and Hibernate S4/S5 state cycling test is done up to 20 cycle each when the board support it. At least 2 loops of all test under R.S.T PRO3 to be completed.**

**Test Notes:**
- Min configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1333MHz
- Max configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1333MHz